

Electronic Acknowledgement Receipt

EFS ID:	3545785
Application Number:	10824329
International Application Number:	
Confirmation Number:	5396
Title of Invention:	Method for treating semiconductor processing components and components formed thereby
First Named Inventor/Applicant Name:	Yeshwanth Narendar
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Filer:	Jeffrey S. Abel
Filer Authorized By:	
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RAM confirmation Number	5282
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Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Amendment After Final	1035-E4371_Response.pdf	186033	no	4
			9684be3ebda520264ba7ee4f46dfcac3c0144d32		
Warnings:					
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2	Extension of Time	1035-E4371_EOT.pdf	78683	no	1
			0d8f4c99145eb87bd1b2f32d2812dfeaae7c3723		
Warnings:					
Information:					
3	Request for Continued Examination (RCE)	1035-E4371_RCE_Transmittal.pdf	643267	no	3
			165761ac2cf053f85097816c1f553d10c2a1cc2c		
Warnings:					
Information:					
4	Oath or Declaration filed	1035-E4371_Rule_132_Declaration_Final.pdf	192447	no	5
			f7f8f6c52cfcb221dd95509fe10f9d3fcc80671b		
Warnings:					
Information:					
5	Information Disclosure Statement (IDS) Filed	1035-E4371_IDS.pdf	792013	no	4
			f709e0998d0ce1a105107967d556427af09eaa09		
Warnings:					
Information:					
6	Information Disclosure Statement Letter	1035-E4371_IDS_Transmittal.pdf	128470	no	3
			e350e5645e9997c6fe104351fdae8df123fe8a52		
Warnings:					
Information:					
7	Fee Worksheet (PTO-06)	fee-info.pdf	8328	no	2
			069e0240b0dc329a5baff8a06c496a2204b605eb		
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